Attansic Technology Corporation.

ATXP1

Jumper Free Over Clock Controller

Data Sheet

Release Date: Sep. 2001
Revision: 1.01

1. General Description

ATXP1 is a full feature of over clocking device. It integrates all functions that are possible to be utilized for over-clocking purpose.

2. Features

- Provide Five VID Input (VIDIN0-4) and Five VID Output (VIDOUT0-4) Pins
- Provide Four FID Input (FIDIN0-3) and Eight Output Pins (FIDOCS0-3 & FIDOCPU0-3)
  - FIDCS0-3 Outputs for Chip Set & FIDCPU0-3 Outputs for CPU
- Provide NBPWROK pin
- Provide Twelve GPIO Pins
- Support Auto-Recover
  - Build-in Watch Dog Timer & Reset Output Signal Pin
- Provide CPU Changing Detect Pin (SLOTOCC#)
- SM Bus Interface
- Package: SSOP 48-Pin

3. Pin Configuration

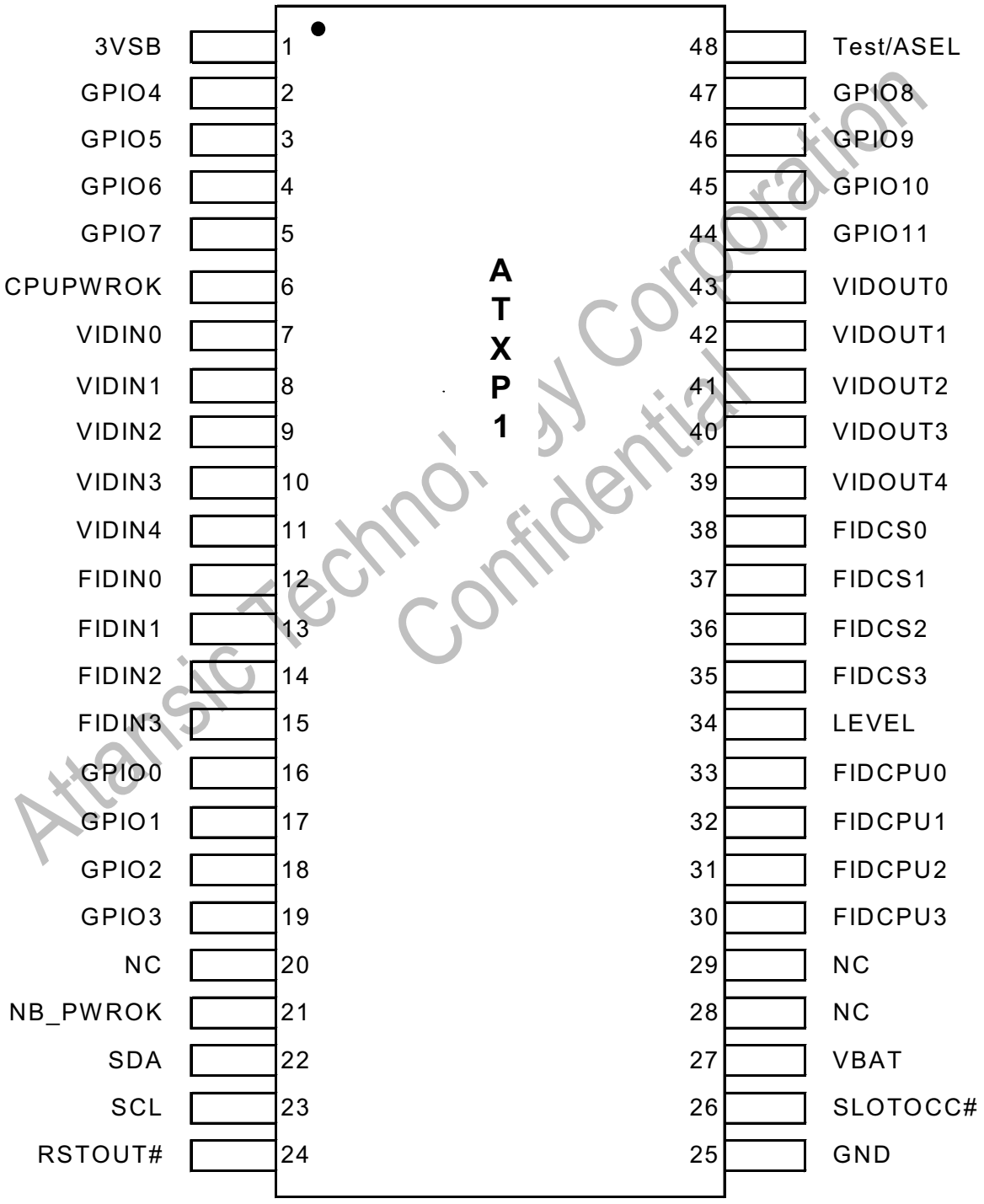
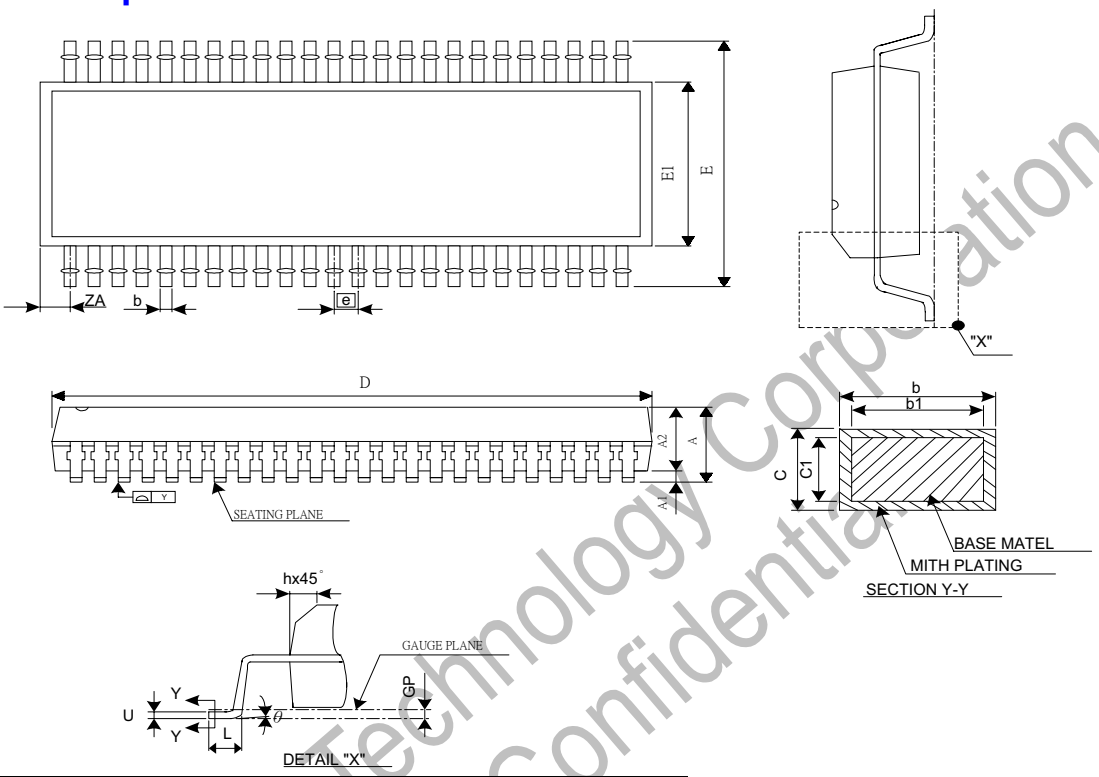


Figure 1. ATXP1 Pin Diagram (Top View)

6. Package Information

SSOP48pin Outline Dimension



Symbol	Dimension (MM)			Dimension (MIL)		
	Min	Nom	Max	Min	Nom	Max
A	2.413	2.591	2.794	95	102	110
A1	0.203	0.305	0.406	8	12	16
A2	2.235	2.286	2.337	88	90	92
b	0.203		0.343	8		13.5
b1	0.203	0.254	0.305	8	10	12
c	0.127		0.254	5		10
c1	0.127	0.203	0.216	5	8	8.5
D	15.748	15.875	16.002	620	625	630
E	10.033	10.312	10.668	395	406	420
E1	7.391	7.493	7.595	291	295	299
e	0.635 BCS			25 BCS		
GP	0.254 BASIC			10 BASIC		
ZD	0.635 REF			25 REF		
h	0.381	0.508	0.635	15	20	25
L	0.508	0.762	1.016	20	30	40
Y			0.102			4
θ	0°	4°	8°	0°	4°	8°

Notes:
1. PEFER TO JEDEC M0-118 AA.
2. DIMENSION “D” DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS MOLD FLASH, PROTURSIONS AND GATE BURRS SHALL NOT EXCEED 0.15mm (6MIL) PER SIDE.
3. DIMENSION “E” DOES NOT INCLUDE INTER-LEAD FLASH OR PROTURSIONS LEAD FLASH AND PROTURSIONS SHALL NOT EXCEED 0.25mm (10MIL) PER SIDE.
4. CONTROLLING DIMENSION: MILLIMETERS.

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