

# N-Channel 60-V (D-S) MOSFET

## PRODUCT SUMMARY

$V_{DS}$ (V)	$R_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)	$Q_g$ (Typ.)
60	0.012 at $V_{GS} = 10$ V	60 <sup>d</sup>	33

## FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- 100 %  $R_g$  Tested
- 100 % UIS Tested
- Compliant to RoHS Directive 2002/95/EC

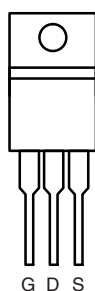


**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**  
Available

## APPLICATIONS

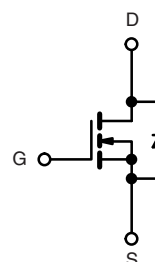
- Synchronous Rectifier
- Power Supplies

TO-220AB



Top View

**Ordering Information:** SUP60N06-12P-E3 (Lead (Pb)-free)  
SUP60N06-12P-GE3 (Lead (Pb)-free and Halogen-free)



N-Channel MOSFET

## ABSOLUTE MAXIMUM RATINGS $T_C = 25^\circ\text{C}$ , unless otherwise noted

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	60	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current ( $T_J = 150^\circ\text{C}$ )	$I_D$	60 <sup>d</sup>	A
		54 <sup>d</sup>	
Pulsed Drain Current	$I_{DM}$	80	
Avalanche Current	$I_{AS}$	40	
Single Avalanche Energy <sup>a</sup>	$E_{AS}$	80	mJ
Maximum Power Dissipation <sup>a</sup>	$P_D$	100 <sup>b</sup>	W
		3.25	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	- 55 to 150	$^\circ\text{C}$

## THERMAL RESISTANCE RATINGS

Parameter	Symbol	Limit	Unit
Junction-to-Ambient (PCB Mount) <sup>c</sup>	$R_{thJA}$	40	$^\circ\text{C}/\text{W}$
Junction-to-Case (Drain)	$R_{thJC}$	1.25	

Notes:

- Duty cycle  $\leq 1\%$ .
- See SOA curve for voltage derating.
- When Mounted on 1" square PCB (FR-4 material).
- Package limited.

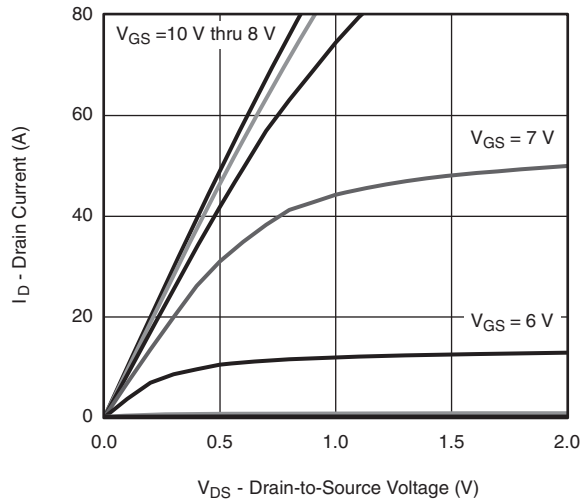
SPECIFICATIONS $T_J = 25\text{ }^{\circ}\text{C}$ , unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{DS} = 0\text{ V}$ , $I_D = 250\text{ }\mu\text{A}$	60			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$ , $I_D = 250\text{ }\mu\text{A}$	2.5		4.5	
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0\text{ V}$ , $V_{GS} = \pm 20\text{ V}$			$\pm 250$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 60\text{ V}$ , $V_{GS} = 0\text{ V}$			1	$\mu\text{A}$
		$V_{DS} = 60\text{ V}$ , $V_{GS} = 0\text{ V}$ , $T_J = 125\text{ }^{\circ}\text{C}$			50	
		$V_{DS} = 60\text{ V}$ , $V_{GS} = 0\text{ V}$ , $T_J = 150\text{ }^{\circ}\text{C}$			250	
On-State Drain Current <sup>a</sup>	$I_{D(on)}$	$V_{DS} \geq 10\text{ V}$ , $V_{GS} = 10\text{ V}$	80			A
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(on)}$	$V_{GS} = 10\text{ V}$ , $I_D = 30\text{ A}$		0.0098	0.012	$\Omega$
		$V_{GS} = 10\text{ V}$ , $I_D = 30\text{ A}$ , $T_J = 125\text{ }^{\circ}\text{C}$		0.0155	0.019	
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = 15\text{ V}$ , $I_D = 15\text{ A}$		37		S
Dynamic <sup>b</sup>						
Input Capacitance	$C_{iss}$	$V_{GS} = 0\text{ V}$ , $V_{DS} = 30\text{ V}$ , $f = 1\text{ MHz}$		1970		pF
Output Capacitance	$C_{oss}$			310		
Reverse Transfer Capacitance	$C_{rss}$			110		
Total Gate Charge <sup>c</sup>	$Q_g$	$V_{DS} = 30\text{ V}$ , $V_{GS} = 10\text{ V}$ , $I_D = 20\text{ A}$		33	55	nC
Gate-Source Charge <sup>c</sup>	$Q_{gs}$			11		
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$			9		
Gate Resistance	$R_g$	$f = 1\text{ MHz}$	0.3	1.4	2.8	$\Omega$
Turn-On Delay Time <sup>c</sup>	$t_{d(on)}$	$V_{DD} = 30\text{ V}$ , $R_L = 1.53\text{ }\Omega$ $I_D \cong 20\text{ A}$ , $V_{GEN} = 10\text{ V}$ , $R_g = 1\text{ }\Omega$		11	20	ns
Rise Time <sup>c</sup>	$t_r$			11	20	
Turn-Off Delay Time <sup>c</sup>	$t_{d(off)}$			16	30	
Fall Time <sup>c</sup>	$t_f$			8	15	
Source-Drain Diode Ratings and Characteristics $T_C = 25\text{ }^{\circ}\text{C}$ <sup>b</sup>						
Continuous Current	$I_S$				60	A
Pulsed Current	$I_{SM}$				80	
Forward Voltage <sup>a</sup>	$V_{SD}$	$I_F = 10\text{ A}$ , $V_{GS} = 0\text{ V}$		0.84	1.5	V
Reverse Recovery Time	$t_{rr}$	$I_F = 10\text{ A}$ , $dI/dt = 100\text{ A}/\mu\text{s}$		40	80	ns
Peak Reverse Recovery Current	$I_{RM(REC)}$			3.2	5.0	A
Reverse Recovery Charge	$Q_{rr}$			64	120	nC

## Notes:

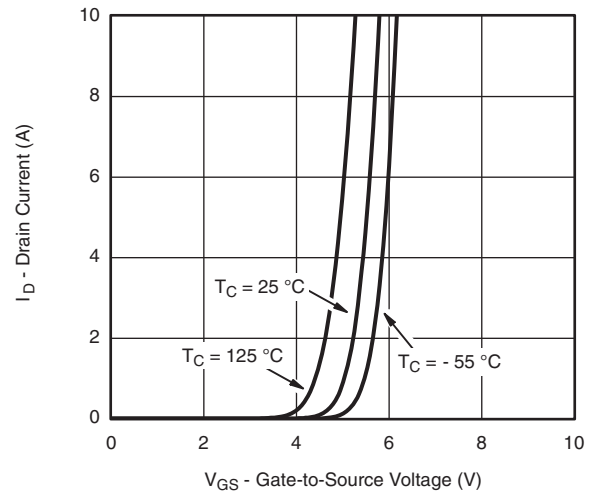
- a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .  
b. Guaranteed by design, not subject to production testing.  
c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

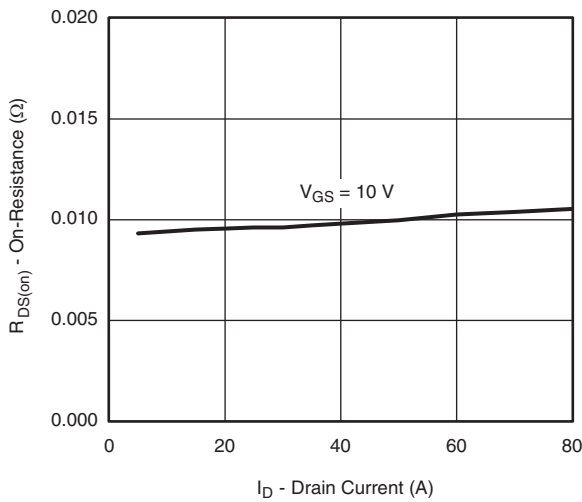
## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



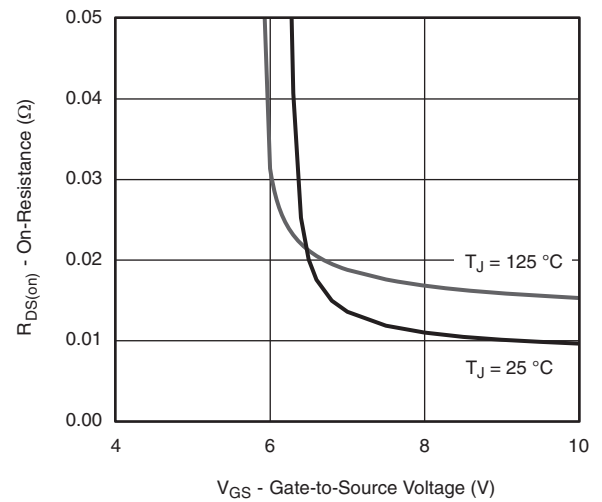
Output Characteristics



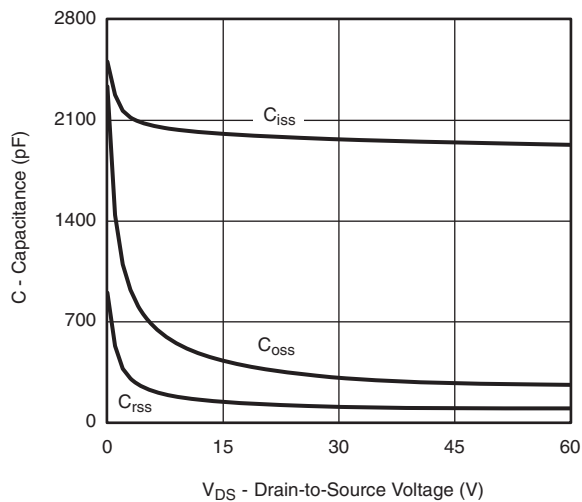
Transfer Characteristics



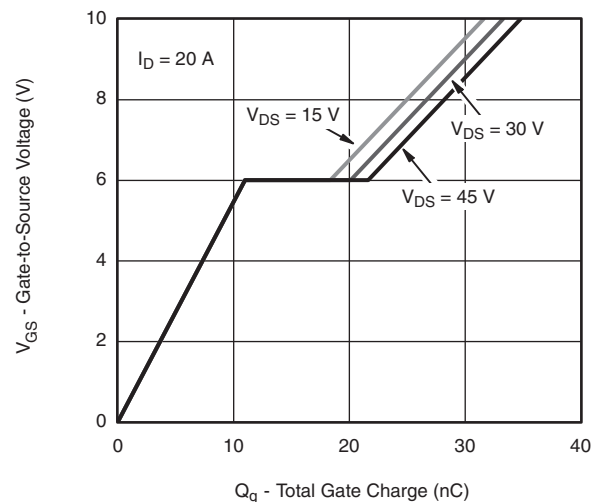
On-Resistance vs. Drain Current



On-resistance vs. Gate-to-Source Voltage

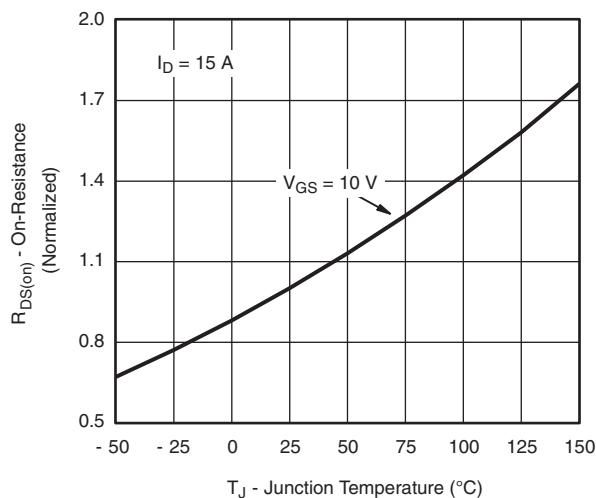


Capacitance

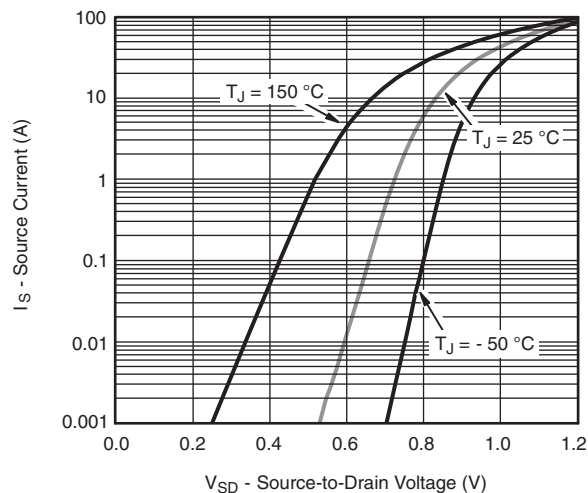


Gate Charge

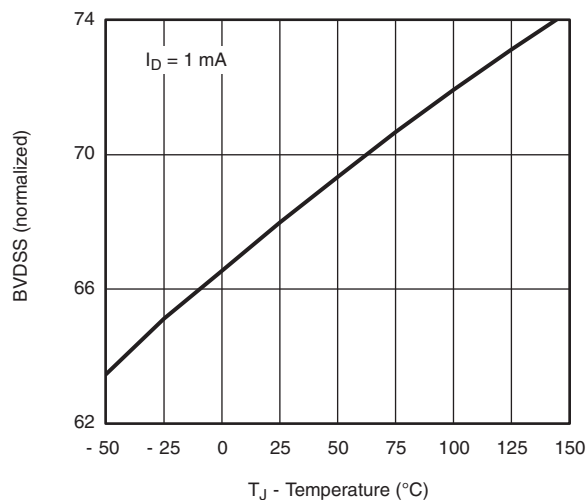
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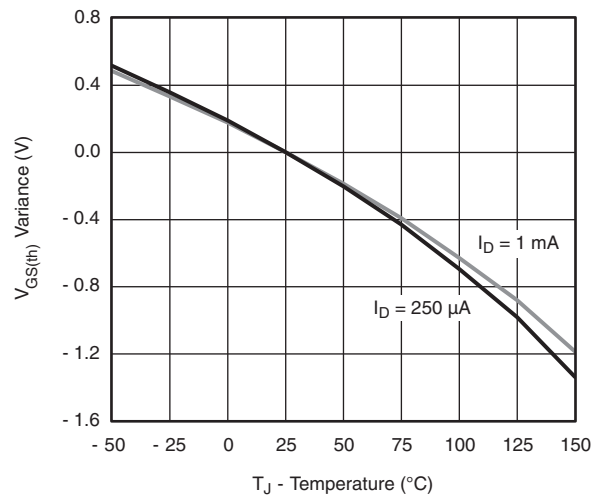
On-Resistance vs. Junction Temperature



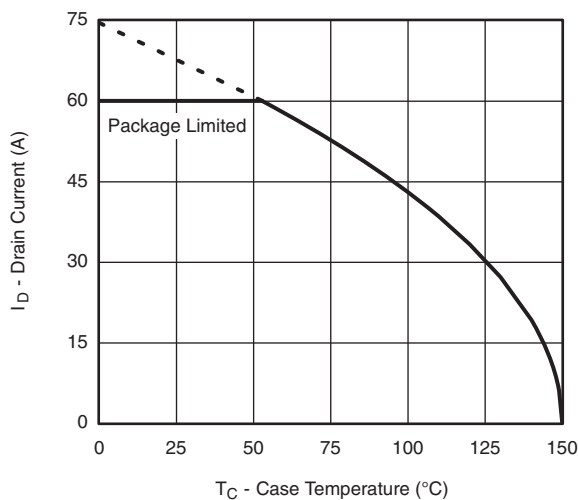
Source-Drain Diode Forward Voltage



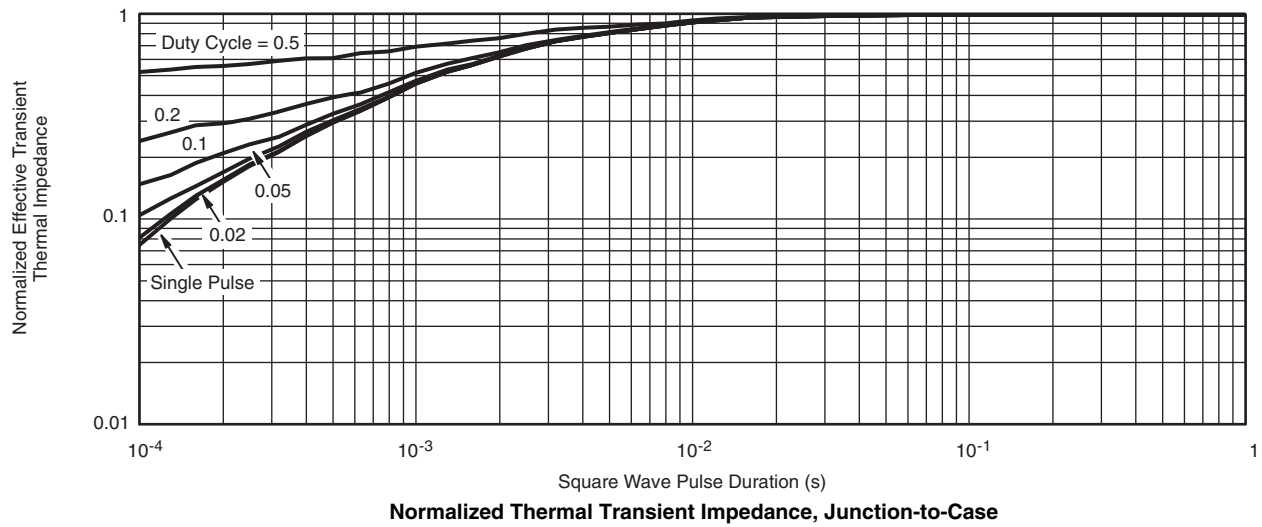
Drain-Source Breakdown vs. Junction Temperature



Threshold Voltage



Maximum Drain Current vs. Case Temperature

**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted


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